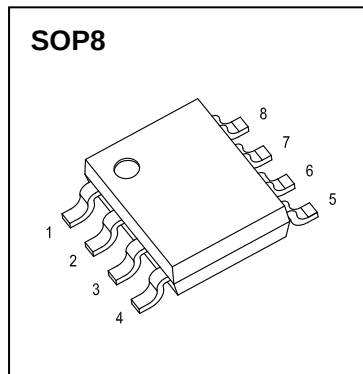


SOP8 Plastic-Encapsulate N- MOSFETS

APPLICATION

- Load Switch
- PWM applications

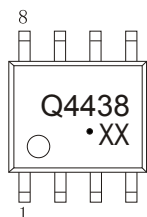
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
60V	18mΩ@10V	8.2A
	25mΩ@4.5V	



FEATURE

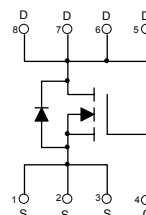
- TrenchFET Power MOSFET
- Low $R_{DS(on)}$
- Low Gate Charge
- ROHS Compliant
- AEC-Q101 qualified (Automotive grade with suffix " Q".)
- Expsemi electronics

MARKING



Q4438 = Device code.
Solid dot = Green molding compound device,
if none, the normal device.
XX = Code.

Equivalent Circuit



MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D ①	8.2	A
Pulsed Drain Current	I_{DM} ②	40	A
Power Dissipation	P_D ⑤	2.5	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$ ⑤	50	$^{\circ}\text{C/W}$
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^{\circ}\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS
T_a=25 °C unless otherwise specified

Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Off characteristics							
Drain-source breakdown voltage	V _{(BR) DSS}	V _{GS} = 0V, I _D =250μA		60			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =48V, V _{GS} =0V	T _J =25℃			1.0	μA
			T _J =125℃			100	
Gate-body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V				±100	nA
On characteristics ③							
Gate-threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA		1.0	1.8	3.0	V
Static drain-source on-sate resistance	R _{DS(on)}	V _{GS} =10V, I _D =8.2A			18	22	mΩ
		V _{GS} =4.5V, I _D =7.6A			25	36	mΩ
Forward transconductance	g _{FS}	V _{DS} =5V, I _D =8.2A					S
Dynamic characteristics ③④							
Input capacitance	C _{iSS}	V _{DS} =30V,V _{GS} =0V, f =1MHz			820	1640	pF
Output capacitance	C _{oSS}				93	186	
Reverse transfer capacitance	C _{rSS}				69	138	
Gate resistance	R _g	f =1MHz			3.8		Ω
Switching characteristics ③④							
Total gate charge	Q _g	V _{GS} =10V, V _{DS} =30V, I _D =6A			21	42	nC
Gate-source charge	Q _{gs}				2.0	4.0	
Gate-drain charge	Q _{gd}				6.6	13	
Turn-on delay time	t _{d(on)}	V _{DS} =30V,R _L =3.6Ω, V _{GS} =10V,R _G =3Ω			8.2		ns
Turn-on rise time	t _r				5.5		
Turn-off delay time	t _{d(off)}				30		
Turn-off fall time	t _f				5.2		
Drain-Source Diode Characteristics							
Drain-source diode forward voltage	V _{SD} ③	V _{GS} =0V, I _S =1A				1.0	V
Continuous drain-source diode forward current	I _S ①					8.2	A
Pulsed drain-source diode forward current	I _{SM} ②					40	A

Notes:

1. T_C=25 °C Limited only by maximum temperature allowed.

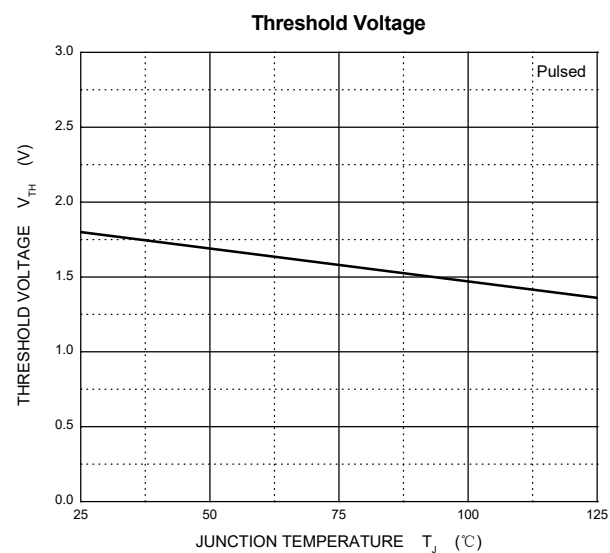
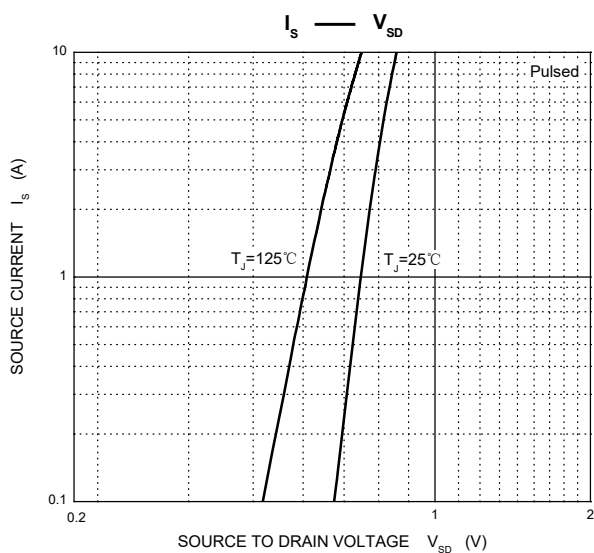
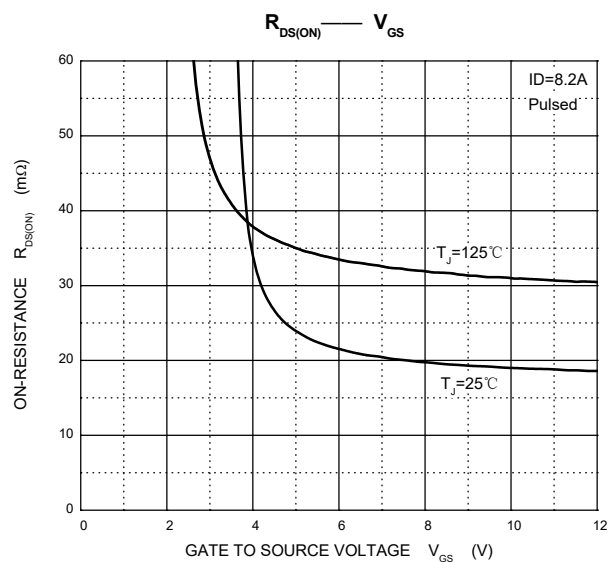
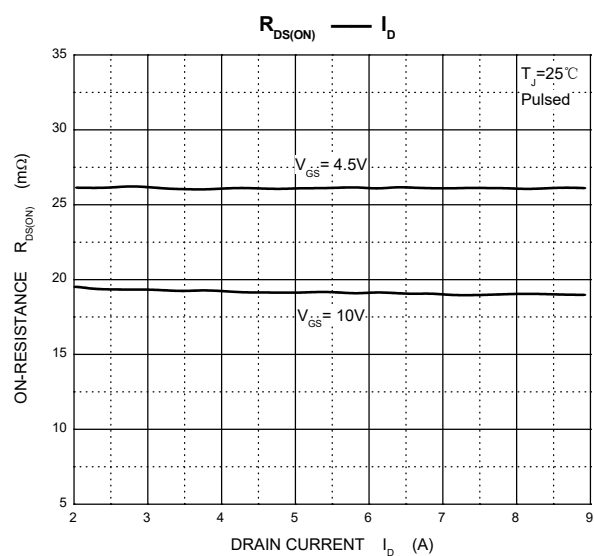
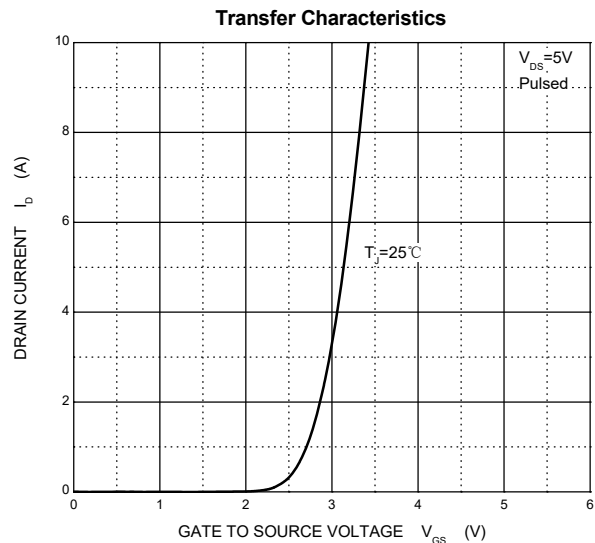
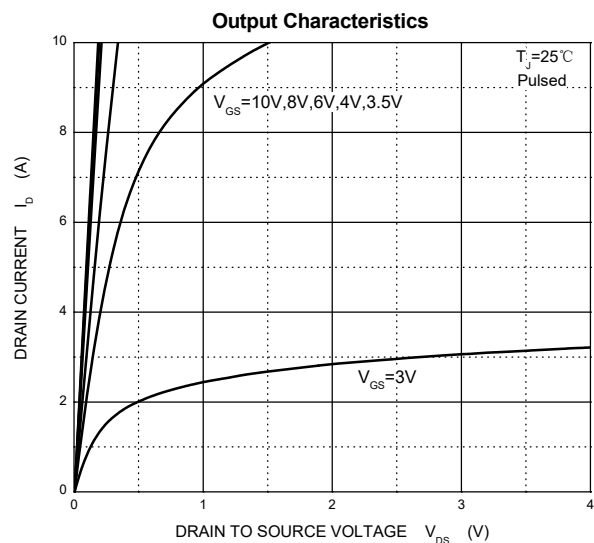
2. P_W≤10μs, Duty cycle≤1%.

3. Pulse Test : Pulse Width≤300μs, duty cycle ≤2%.

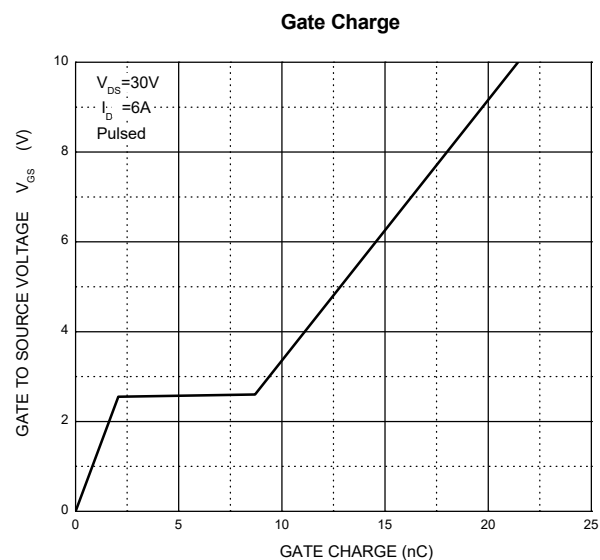
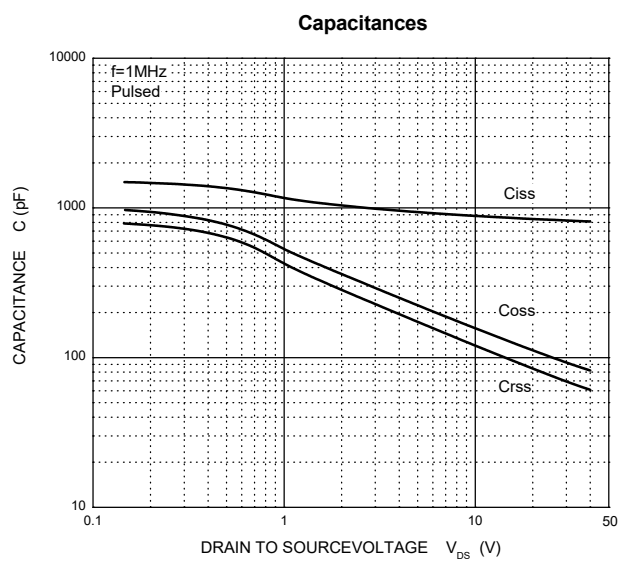
4. Guaranteed by design, not subject to production.

5. The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C, t ≤10sec.

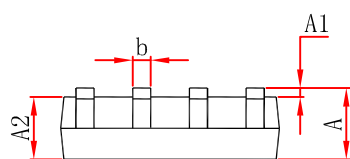
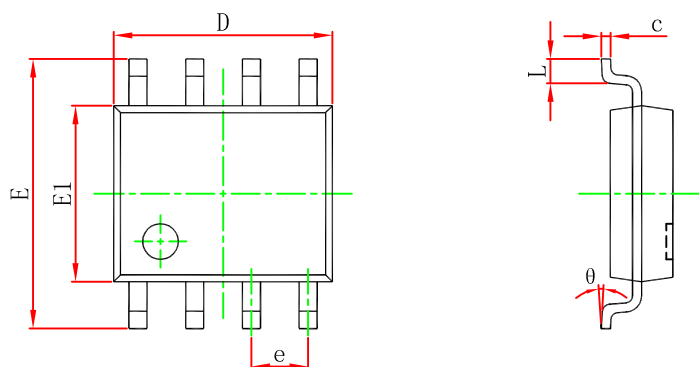
Typical Characteristics



Typical Characteristics

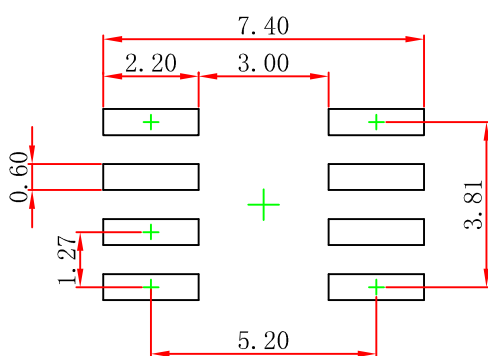


SOP8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

SOP8 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.