

## Product Summary

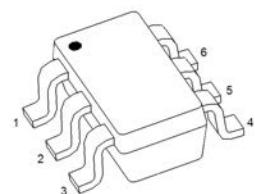
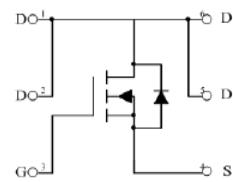
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
100V	77m $\Omega$ @10V	3A
	102m $\Omega$ @4.5V	

## Feature

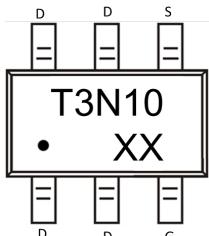
- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge
- AEC-Q101 qualified (Automotive grade with suffix " Q" .)
- Expsemi electronics

## Application

- Load Switch
- DC/DC Converter

**SOT-23-6L**

**Schematic diagram**


## MARKING:



## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

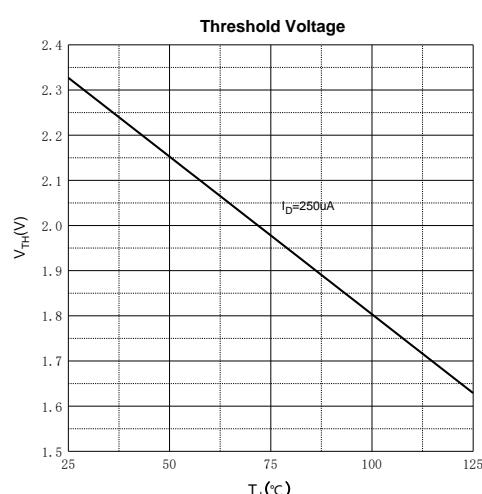
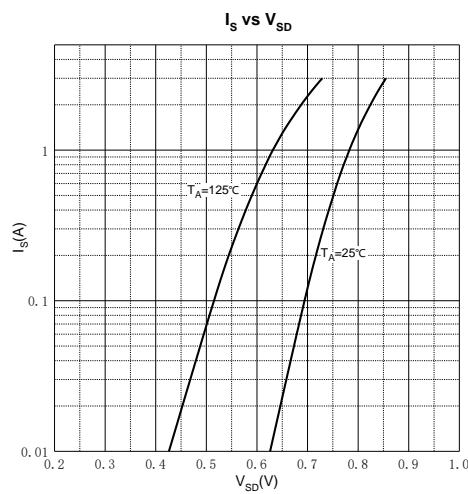
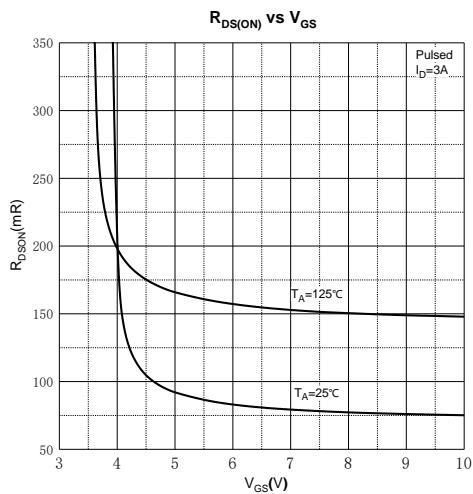
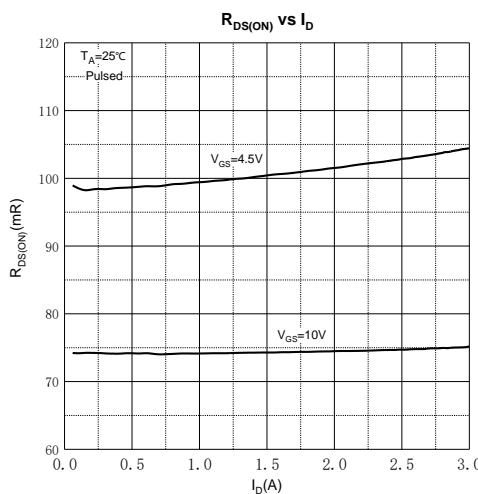
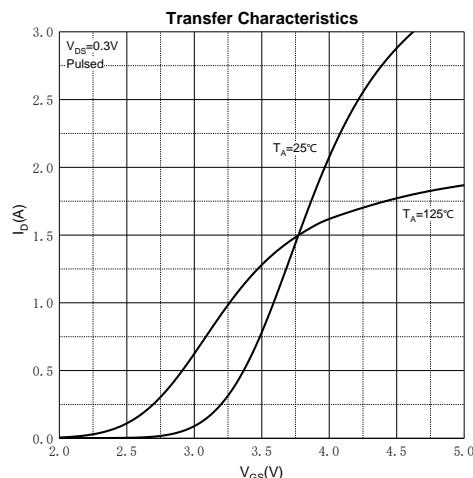
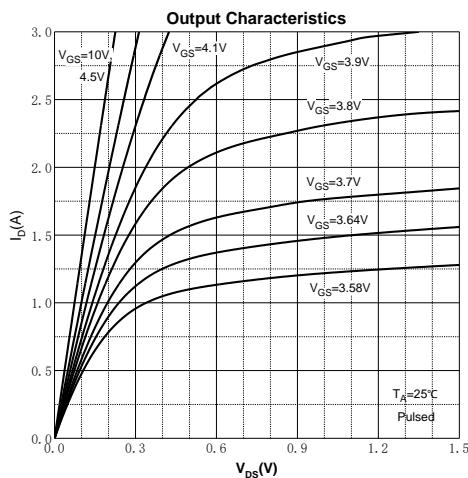
Parameter	Symbol	Value	Unit
Drain - Source Voltage	$V_{DS}$	100	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1,5</sup>	$I_D$	3	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	12	A
Power Dissipation <sup>4,5</sup>	$P_D$	0.8	W
Thermal Resistance from Junction to Ambient <sup>5</sup>	$R_{\theta JA}$	156	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	°C

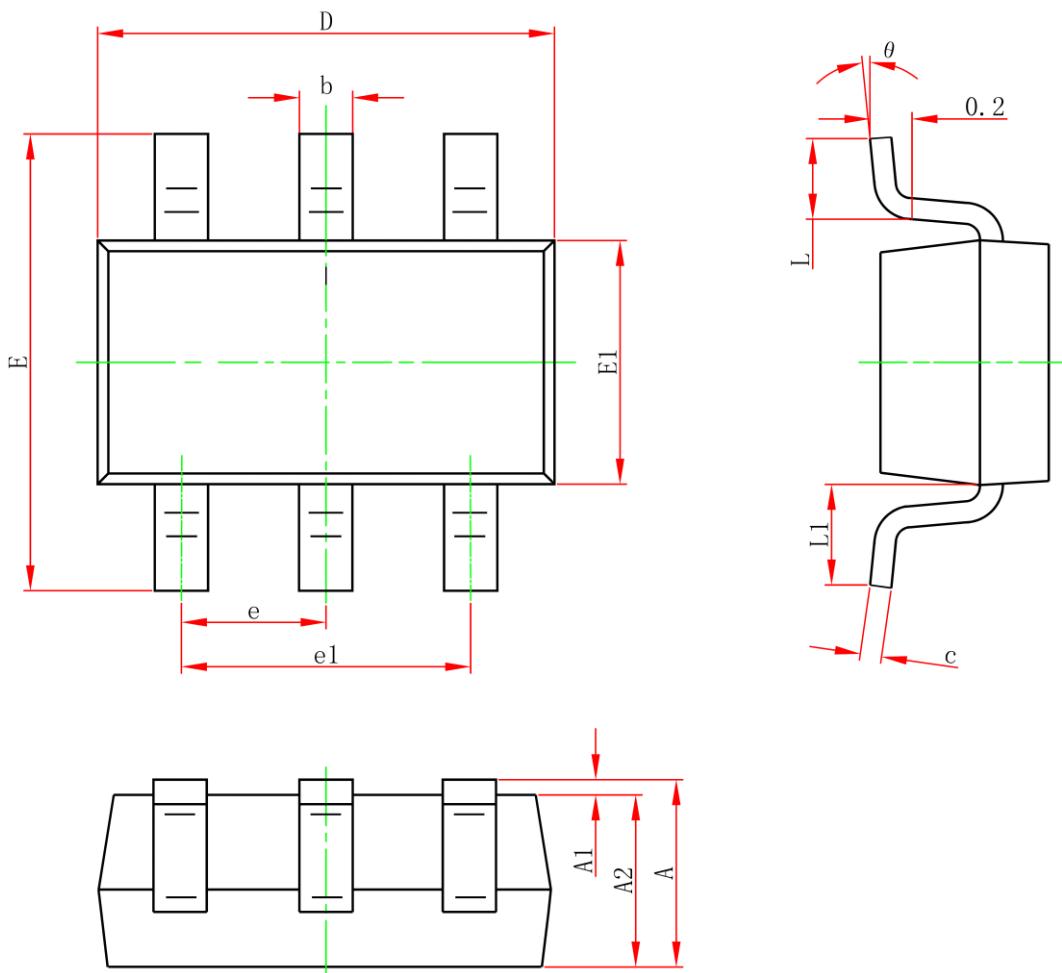
**MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	2	3	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 3\text{A}$		77	145	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 2\text{A}$		102	170	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 45\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		817.2		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			34.3		
Reverse Transfer Capacitance	$C_{\text{rss}}$			30.7		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		1.6		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 3\text{A}$		20.5		$\text{nC}$
Gate-source Charge	$Q_{\text{gs}}$			4.6		
Gate-drain Charge	$Q_{\text{gd}}$			3.3		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 25\text{V}, V_{\text{GS}} = 10\text{V}, R_L = 1\Omega, R_G = 5\Omega$		14.2		$\text{ns}$
Turn-on Rise Time	$t_r$			33.7		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			40.2		
Turn-off Fall Time	$t_f$			6		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 1\text{A}$			1.2	V

Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu\text{s}$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(\text{MAX})} = 150^\circ\text{C}$ .
- 5.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

**Typical Characteristics**


**SOT-23-6L Package Information**


<b>Symbol</b>	<b>Dimensions In Millimeters</b>		<b>Dimensions In Inches</b>	
	<b>Min.</b>	<b>Max.</b>	<b>Min.</b>	<b>Max.</b>
A	1.050	1.250	0.041	0.049
A1	0	0.150	0.000	0.006
A2	1.050	1.250	0.041	0.049
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	2.650	2.950	0.104	0.116
E1	1.500	1.700	0.059	0.067
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
L1	0.600REF		0.024REF	
theta	0°	8°	0°	8°